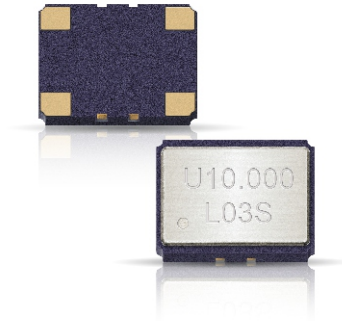
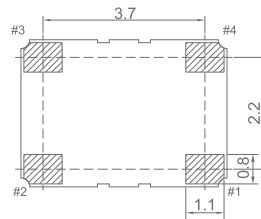
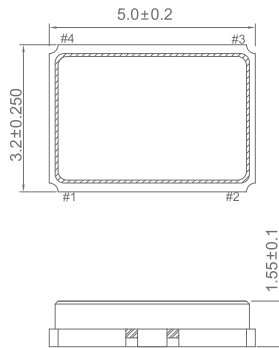


Dimensions(mm)



GENERIC SPECIFICATION

PARAMETER	5.0V		3.3V		UNIT
	Min	Max	Min	Max	
Frequency Range	10MHz to 40.0MHz				MHZ
Standard Frequency (for CMOS)	10,12.8,19.2,20, 26, 30.72				
Standard Frequency (for Clipped Sine)	10,12.8,19.2,20, 26, 30.72				
Frequency Tolerance*	-	±2.0	-	±2.0	ppm
Frequency Stability	Vs Supply Voltage (±5%) change (CMOS)	±0.2	-	±0.2	
	Vs Load (±10%) change	±0.2	-	±0.2	
	Vs Aging (after 1 year)	±1.0	-	±1.0	
Supply Current (CMOS output)	-	6	-	6	mA
Supply Current (Clipped sine wave)	-	3.5	-	3.5	
Output Level (CMOS)	Output High (Logic"1")	90%VDD	-	90%VDD	V
	Output Low (Logic"0")	-	-10%VDD	-	
	Duty	45	55	45	
Output Level (Clipped sine wave)	0.8	-	0.8	-	Vp-p
Load (CMOS)	15pF		15pF		
Load (Clipped sine wave)	10KΩ/10pF		10KΩ/10pF		
Control Voltage Range (VCTCXO)	0.5	2.5	0.5	2.5	V
Pulling Range (VCTCXO)	±5.0	±10.0	±5.0	±10.0	ppm
Vc Input Impedance (VCTCXO)	100	-	100	-	KΩ
Phase Noise @ 12.8 MHz	100 Hz	-125	-	-125	dBc/Hz
	1 KHz	-145	-	-145	
	10 Khz	-150	-	-150	
Start Time	-	2	-	2	mSec
Storage Temp. Range	-55	125	-55	125	°C